



PATENT
0152-0572P

IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant: Kiyoharu KISHIMOTO et al. Conf.: 9527
Appl. No.: 09/902,746 Group: 2827
Filed: July 12, 2001 Examiner: L. Thai
For: SEMICONDUCTOR MODULE AND PRODUCING METHOD THEREFOR

AMENDMENT AND REPLY UNDER 37 C.F.R. § 1.111

Assistant Commissioner for Patents
Washington, DC 20231

January 2, 2003

Sir:

In reply to the Examiner's Office Action dated July 3, 2002, the period for reply being extended three (3) months to January 3, 2003, the following amendments and remarks are respectfully submitted in connection with the above-identified application.

AMENDMENTS

IN THE CLAIMS:

Please cancel claims 1, 2, 3, 7 and 11 without prejudice or disclaimer of the subject matter contained therein.

Please amend the claims as follows:

4. (Amended) A semiconductor module comprising:

at least one electric element including a semiconductor chip;

an electrically conductive layer connected electrically to the

electric element; 01/03/2003 CCHAU1 00000101 022448 09902746

01 FC:1201

168.00 OP

01/03/2003 CCHAU1 00000101 022448 09902746
10.00 CH 920.00 OP